

## *RISE STARTUP Supercomputing Careers & Research Seminars:*

# Monday, March 9 in the HSS Auditorium

Amit Majumdar (majumdar@sdsc.edu), San Diego Supercomputer Center & Dept. of Radiation Medicine and Applied Sciences, UCSD

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## *Careers and Opportunities in (Super)computing*

**Noon-1:00 P.M.**

Refreshments served at 11:30 A.M.

Academic supercomputer centers exist in the US for research and education. This discussion will begin with higher level descriptions of supercomputers, processor architecture, network technologies, and storage technologies, which require different types of research and technology work. This will lead to discussion about career opportunities that are available in supercomputing and computing in general in academic centers, national laboratories and in industry.

## *Supercomputing Research at UCSD*

**3:00-4:00 P.M.**

Refreshments served at 2:30 P.M.

This talk will introduce descriptions of the supercomputers available at UCSD as well as at other NSF funded academic supercomputer centers under the umbrella of the Extreme Science and Engineering Discovery Environment (XSEDE [www.xsede.org](http://www.xsede.org)). Some of the research projects done on San Diego Supercomputer Center's supercomputers will be discussed. We will also discuss how any faculty or researcher from NMSU can gain access to these supercomputers for research and education purposes through the XSEDE allocation process.



*Amit Majumdar is the Director of SDSC's Data Enabled Scientific Computing division and is an Associate Professor in the Department of Radiation Medicine and Applied Sciences at UC San Diego. He received his B.S. from Jadavpur University, Calcutta, India in 1985, M.S. from Idaho State University in 1988 and Ph.D. from University of Michigan in 1996. After working one year at the Ford Research Laboratory in Michigan, he joined the San Diego Supercomputer Center in 1997. His research interests are in high performance computing (HPC), computational science, and cyberinfrastructure software such as science gateways. He has developed parallel algorithms for various kinds of HPC machines and is interested in optimizing, scaling and analyzing scientific applications on HPC machines. He has been involved in computational science projects where medical applications utilize HPC and cyberinfrastructure. One of his current NSF funded projects involves developing a neuroscience gateway (NSG) for computational neuroscientists. He is PI, Co-PI on multiple research projects funded by NSF, AFOSR, and industry (Intel, Microsoft).*

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